Code No: **R20A0563**

MALLA REDDY COLLEGE OF ENGINEERING & TECHNOLOGY

(Autonomous Institution – UGC, Govt. of India)

II B.Tech I Semester Supplementary Examinations, June/July 2024 Computer Organization and Operating Systems

(B.Tech-AIDS)

Roll No					

Time: 3 hours Max. Marks: 70

Note: This question paper Consists of 5 Sections. Answer **FIVE** Questions, Choosing ONE Question from each SECTION and each Question carries 14 marks.

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		SECTION-I	
1	\boldsymbol{A}	Discuss various computer types with their applications in real world	[7M]
		environment.	
	B	Discuss the basic operational concept, in detail.	[7M]
		OR	
2	\boldsymbol{A}	Explain the register transfer bus.	[7M]
	B	Explain instruction cycle with a neat sketch.	[7M]
		<u>SECTION-II</u>	
3	\boldsymbol{A}	Write down the differences between auxiliary memory and associative	[7M]
	ъ	memory.	[5]
	B	Define the virtual memory. What are the advantages?	[7M]
4	4	OR Control of the Con	[#N #1
4	\boldsymbol{A}	Explain the structure of page table.	[7M]
	B	Define operating system. Discuss the various types of operating systems.	[7M]
_		SECTION-III	
5	A	What is linux. Discuss the utilities of linux.	[7M]
	B	Discuss the operations on processes.	[7M]
	4	OR	[#N #1
6	A	What are the various scheduling criteria for CPU scheduling.	[7M]
	В	Explain round robin scheduling algorithm with suitable example.	[7M]
7	4	SECTION-IV Discuss the methods for handling a deadlesk	[7]
/	A B	Discuss the methods for handling a deadlock. Discuss the various file operations, in detail.	[7M] [7M]
	D	OR	[/1/1]
8	\boldsymbol{A}	What is the purpose of using disk attachment in disk management.	[7M]
O	B	Explain FCFS disk scheduling algorithm, with example.	[7M]
	Ъ	SECTION-V	[/141]
9	\boldsymbol{A}	Discuss the design of control unit.	[7M]
	В	Explain the STACK organization model.	[7M]
	_	OR	[··-·-]
10	\boldsymbol{A}	Explain various addressing modes.	[7M]
	B	Explain the architecture of CISC.	[7M]
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